



Docket No.: 67161-039

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
: :
Hideto HIDAKA : Confirmation Number: 9486
: :
Serial No.: 10/615,777 : Group Art Unit: 2811
: :
Filed: July 10, 2003 : Examiner: To be Assigned
: :
For: SEMICONDUCTOR MEMORY DEVICE WITH MAGNETIC DISTURBANCE
REDUCED

REQUEST FOR CORRECTED FILING RECEIPT

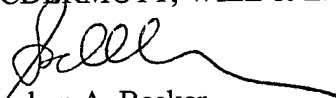
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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached is a copy of the Filing Receipt received from the U.S. Patent and Trademark Office in the above-referenced application. It is noted that the title is incorrect. Attached is a copy of the Declaration and Power of Attorney Document, which evidences that the title should read: **Semiconductor memory device with magnetic disturbance reduced**. It is requested that a corrected filing receipt be issued.

Respectfully submitted,

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/615,777	07/10/2003	2811	750	67161-039	17	11	2

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CONFIRMATION NO. 9486

FILING RECEIPT



OC000000010982184

Date Mailed: 10/03/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Hideto Hidaka, Hyogo, JAPAN;

Assignment For Published Patent Application

RENESAS TECHNOLOGY CORP.;

Domestic Priority data as claimed by applicant

Foreign Applications

JAPAN 2003-026395(P) 02/03/2003

If Required, Foreign Filing License Granted: 10/03/2003

Projected Publication Date: 08/05/2004

Non-Publication Request: No

Early Publication Request: No

Title

device
Semiconductor memory device with magnetic disturbance reduced

Preliminary Class

LICENSE FOR FOREIGN FILING UNDER
Titl 35, Unit d States Code, Section 184
Title 37, Cod of Fed ral Regulations, 5.11 & 5.15

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